



Firmware Engineer

Suricog (founded in 2013) is a high-profile startup which is based in Paris. Suricog develops Head-Up EyeTracking devices for advanced Human Machine Interfaces, and for Healthcare applications. Its solutions are already sold in several countries, including for large companies.

We are currently looking for a Firmware Engineer to help us develop our new 2.0 Connected Glasses (Video Sensor, Microcontroller/FPGA, WIFI/USB Com), allowing launching it on the market. The Firmware engineer will also work on future evolutions and new products.

Tasks for the very short term are the following:

Interacting within a diverse team of engineers (Hardware, Algorithms, 3D Modeling) the main tasks are the following:

- Finalising and tuning the new electronic card main modules:
 - Startup, initialization of the processor, loading of the FPGA, code execution
 - Launch of the image acquisition module
 - Communications between Host and Target by Wifi and USB
- Documentation
- Tests and Validation

Mid-term :

As a member of the core team, the firmware engineer will actively participate in the development of the company strategic evolutions working on:

- Further electronic architecture definitions considering criteria such as real-time performances, consumption, mobility and integration, additional features...
- The definition of a process for Quality/Tests/Validation for the Embedded part
- The technological and Product surveys and evaluations

Profile:

For this position, we are looking for experienced (mid to senior) candidates. Ideal candidate should possess a Bachelor's or Master's degree in Hardware/Software Engineering, Electrical Engineering, Electronics & Embedded Technology or Computer Science, with a minimum of 5 years of relevant experience.

Qualified candidates will also:

- Demonstrate proficient knowledge and utilization of engineering tools necessary for successful performance of the essential functions of this job description
- Must demonstrate proficiency and possess experience necessary to develop, test, debug software in C on ARM processors in an Embedded environment (Arm-Cortex A9, Linux RT, cross-compilation, interruptions, USB and WIFI drivers development, FPGA interfacing)
- Demonstrate mastery of relevant software engineering tools (build processes, debuggers, emulators, simulators and logic analyzers)
- Configuration management tools such as Git



Desirable skills:

- Experience with Image Capture devices
- Knowledge of FPGA programming

Interpersonal skills:

- Curious, ingenious, creative, passionate, open-minded and oriented knowledge sharing
- Good communication skills, including the ability to provide clear and organized documentation for all work performed
- Fluent in French or fluent in verbal and written English with basic in French

This is a full-time French contract which includes all French benefits (Healthcare, Retirement plan...). Freelancers considered.

Work is in our offices, located in Paris Center in a modern startup incubator.

Please send us a cv and a cover letter highlighting your relevant experience and skills at jobs@suricog.com (ref:19EFI)